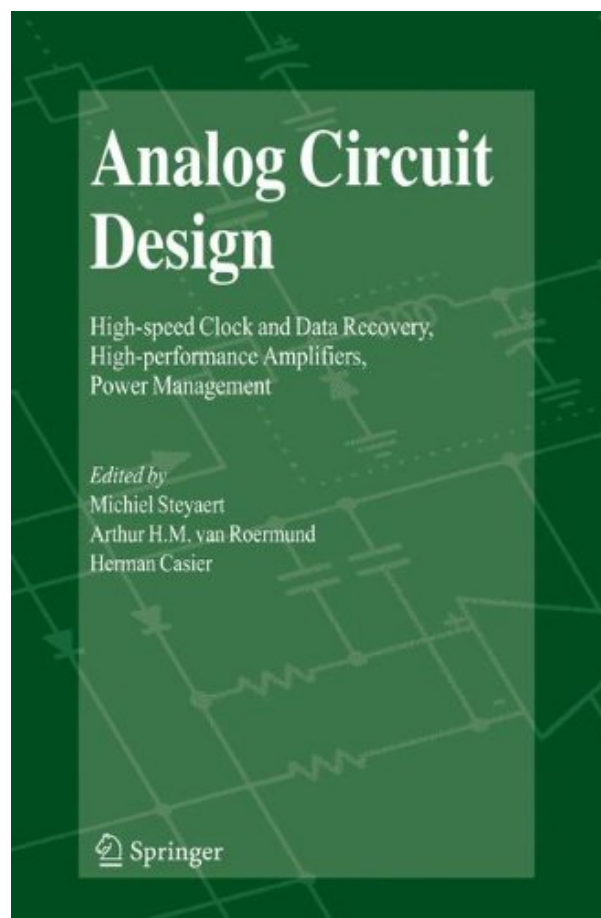


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Analog Circuit Design

High-speed Clock and Data Recovery,
High-performance Amplifiers,
Power Management

Edited by
Michiel Steyaert
Arthur H.M. van Roermund
Herman Casier

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Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field. The tutorial coverage also makes it suitable for use in an advanced design.

About the Author

Prof. Arthur van Roermund and Prof. Michiel Steyaert have been editing the best papers from the yearly Advances in Analog Circuit Design workshop into a book for the past 5 years. Dr. Herman Casier is new to the editing trio, he brings excellent input from industry

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